	Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
	Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)
1500-er CPUs							
TIA-Portal V12							
CPU 1511-1 PN V1.01 (6ES7 511-1AK00-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1511-1 PN V1.1 (6ES7 511-1AK00-0AB0)	30%	2000	25%	500	1500	30	19
CPU1516-3 PN/DP V1.0.1 (6ES7 516-3AN00-0AB0)	30%	4000	25%	1000	3000	83	41
TIA-Portal V12 SP1							
CPU 1511-1 PN V1.1 (6ES7 511-1AK00-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1511F-1 PN V1.1 (6ES7 511-1FK00-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1513-1 PN V1.1 (6ES7 513-1AL00-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1513F-1 PN V1.1 (6ES7 513-1FL00-0AB0)	30%	2000	25%	500	1500	41	27
CPU1516-3 PN/DP V1.1 (6ES7 516-3AN00-0AB0)	30%	4000	25%	1000	3000	83	41
CPU1516F-3 PN/DP V1.1 (6ES7 516-3FN00-0AB0)	30%	4000	25%	1000	3000	83	41
1200-er CPUs							
TIA-Portal V12							
CPU 1211 C AC/DC/Rly V3.0 (6ES7 211-1BE31-0XB0)	30%	165	25%	41	124		3
CPU 1211 C DC/DC/DC V3.0 (6ES7 211-1AE31-0XB0)	30%	165	25%	41	124		3
CPU 1211 C DC/DC/Rly V3.0 (6ES7 211-1HE31-0XB0)	30%	165	25%	41	124		3
CPU 1212 C AC/DC/Rly V3.0 (6ES7 212-1BE31-0XB0)	30%	165	25%	41	124		3
CPU 1212 C DC/DC/DC V3.0 (6ES7 212-1AE31-0XB0)	30%	165	25%	41	124		3
CPU 1212 C DC/DC/Rly V3.0 (6ES7 212-1HE31-0XB0)	30%	165	25%	41	124		3
CPU 1214 C AC/DC/Rly V3.0 (6ES7 214-1BG40-0XB0)	30%	165	25%	41	124		3
CPU 1214 C DC/DC/DC V3.0 (6ES7 214-1AG31-0XB0)	30%	165	25%	41	124		3
CPU 1214 C DC/DC/Rly V3.0 (6ES7 214-1HG31-0XB0)	30%	165	25%	41	124		3
CPU 1215 C AC/DC/Rly V3.0 (6ES7 215-1BG31-0XB0)	30%	165	25%	41	124		3
CPU 1215 C DC/DC/DC V3.0 (6ES7 215-1AG31-0XB0)	30%	165	25%	41	124		3
CPU 1215 C DC/DC/Rly V3.0 (6ES7 215-1HG31-0XB0)	30%	165	25%	41	124		3

	Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
	Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)

1) Diese Zahl ist keine genaue Angabe, sondern nur eine Daumenregel.

Die Anzahl gilt für den eingeschwungenen Zustand, also ohne Bildwechsel. Daher darf die Mindestanzahl temporär niedriger sein; sobald die Zahl gegen 0 geht, beginnt das zyklische Polling und ist die CPU potentiell überlastet.

1) This is not an exact number, just a rule of thumb.

The number is valid for the engaged status, without screen change. This is why the minimum number may be lower temporarily; as soon as the number tends to 0, the cyclic polling begins and the CPU is potentially overloaded.

	Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
	Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)
1500-er CPUs							
TIA-Portal V13							
CPU 1511-1 PN V1.5 (6ES7 511-1AK00-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1513-1 PN V1.5 (6ES7 513-1AL00-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1515-2 PN V1.5 (6ES7515-2AM00-0AB0)	30%	4000	25%	1000	3000	62	34
CPU 1516-3 PN/DP V1.5 (6ES7 516-3AN00-0AB0)	30%	8000	15%	1200	6800	83	41
CPU 1516F-3 PN/DP V1.5 (6ES7 516-3FN00-0AB0)	30%	8000	15%	1200	6800	83	41
CPU 1518-4 PN/DP V1.5 (6ES7 518-4AP00-0AB0)	30%	12000	15%	1800	10200	126	62
CPU 1518F-4 PN/DP V1.5 (6ES7 518-4FP00-0AB0)	30%	12000	15%	1800	10200	126	62
TIA-Portal V13 Update2							
CPU 1510SP-1 PN (6ES7 510-1DJ00-0AB0)	30%	2000	25%	500	1500		19
CPU 1510SP F-1 PN (6ES7 510-1SJ00-0AB0)	30%	2000	25%	500	1500		19
CPU 1511-1 PN (6ES7 511-1AK00-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1511F-1 PN (6ES7 511-1FK00-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1512SP-1 PN (6ES7 512-1DK00-0AB0)	30%	2000	25%	500	1500		27
CPU 1512SP F-1 PN (6ES7 512-1SK00-0AB0)	30%	2000	25%	500	1500		27
CPU 1513-1 PN (6ES7 513-1AL00-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1513F-1 PN (6ES7 513-1FL00-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1515-2 PN (6ES7515-2AM00-0AB0)	30%	4000	25%	1000	3000	62	34
CPU 1515F-2 PN (6ES7515-2FM00-0AB0)	30%	4000	25%	1000	3000	62	34
CPU 1516-3 PN/DP (6ES7 516-3AN00-0AB0)	30%	8000	15%	1200	6800	83	41
CPU 1516F-3 PN/DP (6ES7 516-3FN00-0AB0)	30%	8000	15%	1200	6800	83	41
CPU 1517-3 PN/DP (6ES7 517-3AP00-0AB0)	30%	12000	15%	1800	10200	105	51
CPU 1517F-3 PN/DP (6ES7 517-3FP00-0AB0)	30%	12000	15%	1800	10200	105	51
CPU 1518-4 PN/DP (6ES7 518-4AP00-0AB0)	30%	12000	15%	1800	10200	126	62
CPU 1518F-4 PN/DP (6ES7 518-4FP00-0AB0)	30%	12000	15%	1800	10200	126	62
TIA-Portal V13 SP1							
CPU 1513-1 PN (6ES7 513-1AL00-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1513F-1 PN (6ES7 513-1FL00-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1515-2 PN (6ES7515-2AM00-0AB0)	30%	4000	25%	1000	3000	62	34
CPU 1515F-2 PN (6ES7515-2FM00-0AB0)	30%	4000	25%	1000	3000	62	34

	Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
	Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)
CPU 1516-3 PN/DP (6ES7 516-3AN00-0AB0)	30%	8000	15%	1200	6800	83	41
CPU 1516F-3 PN/DP (6ES7 516-3FN00-0AB0)	30%	8000	15%	1200	6800	83	41
CPU 1517-3 PN/DP (6ES7 517-3AP00-0AB0)	30%	12000	15%	1800	10200	105	51
CPU 1517F-3 PN/DP (6ES7 517-3FP00-0AB0)	30%	12000	15%	1800	10200	105	51
CPU 1518-4 PN/DP (6ES7 518-4AP00-0AB0)	30%	12000	15%	1800	10200	126	62
CPU 1518F-4 PN/DP (6ES7 518-4FP00-0AB0)	30%	12000	15%	1800	10200	126	62
TIA-Portal V13 Update4							
CPU1505SP FailSafe (6ES7 672-5SC01-0YA0)	30%	8000	15%	1200	6800		27
CPU1505S FailSafe (6ES7 672-5FC01-0YA0)	30%	8000	15%	1200	6800		27
CPU1505SP (6ES7 672-5DC01-0YA0)	30%	8000	15%	1200	6800		27
CPU1505S (6ES7 672-5AC01-0YA0)	30%	8000	15%	1200	6800		27
CPU1505S (6ES7 672-5AC01-0YA0)	30%	8000	15%	1200	6800		27
CPU1507S FailSafe (6ES7 672-7FC01-0YA0)	30%	8000	15%	1200	6800		41
CPU1507S (6ES7 672-7AC01-0YA0)	30%	8000	15%	1200	6800		41
CPU 1510SP-1 PN (6ES7 510-1DJ01-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1510SP F-1 PN (6ES7 510-1SJ01-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1511-1 PN (6ES7 511-1AK01-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1511-1 PN (6ES7 511-1AK01-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1511F-1 PN (6ES7 511-1FK01-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1511T-1 PN (6ES7 511-1TK01-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1511C-1 PN (6ES7511-1CK00-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1512C-1 PN (6ES7512-1CK00-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1512SP-1 PN (6ES7 512-1DK01-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1512SP F-1 PN (6ES7 512-1SK01-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1513-1 PN (6ES7 513-1AL01-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1513F-1 PN (6ES7 513-1FL00-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1513F-1 PN (6ES7 513-1FL01-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1513R-1 PN (6ES7 513-1RL00-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1515-2 PN (6ES7515-2AM01-0AB0)	30%	4000	25%	1000	3000	62	34
CPU 1515F-2 PN (6ES7515-2FM01-0AB0)	30%	4000	25%	1000	3000	62	34
CPU 1515R-2 PN (6ES7515-2RM00-0AB0)	30%	4000	25%	1000	3000	62	34

	Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
	Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)
CPU 1515T-2 PN (6ES7515-2TM01-0AB0)	30%	4000	25%	1000	3000	62	34
CPU 1516-3 PN/DP (6ES7 516-3AN01-0AB0)	30%	8000	15%	1200	6800	83	41
CPU 1516F-3 PN/DP (6ES7 516-3FN01-0AB0)	30%	8000	15%	1200	6800	83	41
CPU 1516pro-2 PN (6ES7 516-2PN00-0AB0)	30%	8000	15%	1200	6800		41
CPU 1516pro F-2 PN (6ES7 516-2GN00-0AB0)	30%	8000	15%	1200	6800		41
CPU 1517-3 PN/DP (6ES7 517-3AP00-0AB0)	30%	12000	15%	1800	10200	105	51
CPU 1517F-3 PN/DP (6ES7 517-3FP00-0AB0)	30%	12000	15%	1800	10200	105	51
CPU 1517T-3 PN/DP (6ES7 517-3TP00-0AB0)	30%	12000	15%	1800	10200	105	51
CPU 1517TF-3 PN/DP (6ES7 517-3UP00-0AB0)	30%	12000	15%	1800	10200	105	51
CPU 1518-4 PN/DP (6ES7 518-4AP00-0AB0)	30%	12000	15%	1800	10200	126	62
CPU 1518F-4 PN/DP (6ES7 518-4FP00-0AB0)	30%	12000	15%	1800	10200	126	62
CPU 1518-4 PN/DP ODK (6ES7 518-4AP00-3AB0)	30%	12000	15%	1800	10200	126	62
CPU 1518F-4 PN/DP ODK (6ES7 518-4FP00-3AB0)	30%	12000	15%	1800	10200	126	62
1200-er CPUs							
TIA-Portal V13							
CPU 1211 C AC/DC/Rly V4.0 (6ES7 211-1BE40-0XB0)	20%	400	25%	100	300		4
CPU 1211 C DC/DC/DC V4.0 (6ES7 211-1AE40-0XB0)	20%	400	25%	100	300		4
CPU 1211 C DC/DC/Rly V4.0 (6ES7 211-1HE40-0XB0)	20%	400	25%	100	300		4
CPU 1212 C AC/DC/Rly V4.0 (6ES7 212-1BE40-0XB0)	20%	400	25%	100	300		4
CPU 1212 C DC/DC/DC V4.0 (6ES7 212-1AE40-0XB0)	20%	400	25%	100	300		4
CPU 1212 C DC/DC/Rly V4.0 (6ES7 212-1HE40-0XB0)	20%	400	25%	100	300		4
CPU 1214 C AC/DC/Rly V4.0 (6ES7 214-1BG40-0XB0)	20%	400	25%	100	300		4
CPU 1214 C DC/DC/DC V4.0 (6ES7 214-1AG40-0XB0)	20%	400	25%	100	300		4
CPU 1214 C DC/DC/Rly V4.0 (6ES7 214-1HG40-0XB0)	20%	400	25%	100	300		4
CPU 1215 C AC/DC/Rly V4.0 (6ES7 215-1BG40-0XB0)	20%	400	25%	100	300		4
CPU 1215 C DC/DC/DC V4.0 (6ES7 215-1AG40-0XB0)	20%	400	25%	100	300		4
CPU 1215 C DC/DC/Rly V4.0 (6ES7 215-1HG40-0XB0)	20%	400	25%	100	300		4
CPU 1217 C DC/DC/DC V4.0 (6ES7 217-1AG40-0XB0)	20%	400	25%	100	300		4
TIA-Portal V13 SP1							

	Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
	Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)
CPU 1211C AC/DC/Rly (6ES7 211-1BE40-0XB0)	20%	400	25%	100	300		7
CPU 1211C DC/DC/DC (6ES7 211-1AE40-0XB0)	20%	400	25%	100	300		7
CPU 1211C DC/DC/Rly (6ES7 211-1HE40-0XB0)	20%	400	25%	100	300		7
CPU 1212C AC/DC/Rly (6ES7 212-1BE40-0XB0)	20%	400	25%	100	300		7
CPU 1212C DC/DC/DC (6ES7 212-1AE40-0XB0)	20%	400	25%	100	300		7
CPU 1212C DC/DC/Rly (6ES7 212-1HE40-0XB0)	20%	400	25%	100	300		7
CPU 1214C AC/DC/Rly (6ES7 214-1BG40-0XB0)	20%	400	25%	100	300		7
CPU 1214C DC/DC/DC (6ES7 214-1AG40-0XB0)	20%	400	25%	100	300		7
CPU 1214C DC/DC/Rly (6ES7 214-1HG40-0XB0)	20%	400	25%	100	300		7
CPU 1215C AC/DC/Rly (6ES7 215-1BG40-0XB0)	20%	400	25%	100	300		7
CPU 1215C DC/DC/DC (6ES7 215-1AG40-0XB0)	20%	400	25%	100	300		7
CPU 1215C DC/DC/Rly (6ES7 215-1HG40-0XB0)	20%	400	25%	100	300		7
CPU 1217C DC/DC/DC (6ES7 217-1AG40-0XB0)	20%	400	25%	100	300		7
CPU 1214FC DC/DC/DC (6ES7 214-1AF40-0XB0)	20%	400	25%	100	300		7
CPU 1214FC DC/DC/Rly (6ES7 214-1HF40-0XB0)	20%	400	25%	100	300		7
CPU 1215FC DC/DC/DC (6ES7 215-1AF40-0XB0)	20%	400	25%	100	300		7
CPU 1215FC DC/DC/Rly (6ES7 215-1HF40-0XB0)	20%	400	25%	100	300		7

	Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
	Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)

1) Diese Zahl ist keine genaue Angabe, sondern nur eine Daumenregel.

Die Anzahl gilt für den eingeschwungenen Zustand, also ohne Bildwechsel. Daher darf die Mindestanzahl temporär niedriger sein; sobald die Zahl gegen 0 geht, beginnt das zyklische Polling und ist die CPU potentiell überlastet.

1) This is not an exact number, just a rule of thumb.

The number is valid for the engaged status, without screen change. This is why the minimum number may be lower temporarily; as soon as the number tends to 0, the cyclic polling begins and the CPU is potentially overloaded.

	Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
	Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)
1500-er CPUs							
TIA-Portal V14							
CPU1505SP FailSafe (6ES7 672-5SC01-0YA0)	30%	8000	15%	1200	6800	27	27
CPU1505S FailSafe (6ES7 672-5FC01-0YA0)	30%	8000	15%	1200	6800	27	27
CPU1505SP (6ES7 672-5DC01-0YA0)	30%	8000	15%	1200	6800	27	27
CPU1505S (6ES7 672-5AC01-0YA0)	30%	8000	15%	1200	6800	27	27
CPU1507S FailSafe (6ES7 672-7FC01-0YA0)	30%	8000	15%	1200	6800	41	41
CPU1507S (6ES7 672-7AC01-0YA0)	30%	8000	15%	1200	6800	41	41
CPU 1510SP-1 PN (6ES7 510-1DJ01-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1510SP F-1 PN (6ES7 510-1SJ01-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1511-1 PN (6ES7 511-1AK00-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1511-1 PN (6ES7 511-1AK01-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1511F-1 PN (6ES7 511-1FK01-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1511T-1 PN (6ES7 511-1TK01-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1511C-1 PN (6ES7511-1CK00-0AB0)	30%	2000	25%	500	1500	30	19
CPU 1512C-1 PN (6ES7512-1CK00-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1512SP-1 PN (6ES7 512-1DK01-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1512SP F-1 PN (6ES7 512-1SK01-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1513-1 PN (6ES7 513-1AL01-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1513F-1 PN (6ES7 513-1FL01-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1513R-1 PN (6ES7 513-1RL00-0AB0)	30%	2000	25%	500	1500	41	27
CPU 1515-2 PN (6ES7515-2AM01-0AB0)	30%	4000	25%	1000	3000	62	34
CPU 1515F-2 PN (6ES7515-2FM01-0AB0)	30%	4000	25%	1000	3000	62	34
CPU 1515R-2 PN (6ES7515-2RM00-0AB0)	30%	4000	25%	1000	3000	62	34
CPU 1515T-2 PN (6ES7515-2TM01-0AB0)	30%	4000	25%	1000	3000	62	34
CPU 1516-3 PN/DP(6ES7 516-3AN01-0AB0)	30%	8000	15%	1200	6800	83	41
CPU 1516F-3 PN/DP (6ES7 516-3FN01-0AB0)	30%	8000	15%	1200	6800	83	41
CPU 1516pro-2 PN (6ES7 516-2PN00-0AB0)	30%	8000	15%	1200	6800	83	41
CPU 1516pro F-2 PN (6ES7 516-2GN00-0AB0)	30%	8000	15%	1200	6800	41	41
CPU 1516pro F-2 PN (6ES7 516-2GN00-0AB0)	30%	8000	15%	1200	6800	41	41
CPU 1517-3 PN/DP (6ES7 517-3AP00-0AB0)	30%	20000	15%	3000	17000	105	51
CPU 1517F-3 PN/DP (6ES7 517-3FP00-0AB0)	30%	20000	15%	3000	17000	105	51

	Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
	Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)
CPU 1517T-3 PN/DP (6ES7 517-3TP00-0AB0)	30%	20000	15%	3000	17000	105	51
CPU 1517TF-3 PN/DP (6ES7 517-3UP00-0AB0)	30%	20000	15%	3000	17000	105	51
CPU 1518-4 PN/DP (6ES7 518-4AP00-0AB0)	30%	40000	15%	6000	34000	126	62
CPU 1518F-4 PN/DP (6ES7 518-4FP00-0AB0)	30%	40000	15%	6000	34000	126	62
CPU 1518-4 PN/DP ODK (6ES7 518-4AP00-3AB0)	30%	40000	15%	6000	34000	126	62
CPU 1518F-4 PN/DP ODK (6ES7 518-4FP00-3AB0)	30%	40000	15%	6000	34000	126	62
1200-er CPUs							
TIA-Portal V14							
CPU 1211C AC/DC/Rly (6ES7 211-1BE40-0XB0)	20%	400	25%	100	300		4
CPU 1211C DC/DC/DC (6ES7 211-1AE40-0XB0)	20%	400	25%	100	300		4
CPU 1211C DC/DC/Rly (6ES7 211-1HE40-0XB0)	20%	400	25%	100	300		4
CPU 1212C AC/DC/Rly (6ES7 212-1BE40-0XB0)	20%	400	25%	100	300		4
CPU 1212C DC/DC/DC (6ES7 212-1AE40-0XB0)	20%	400	25%	100	300		4
CPU 1212C DC/DC/Rly (6ES7 212-1HE40-0XB0)	20%	400	25%	100	300		4
CPU 1214C AC/DC/Rly (6ES7 214-1BG40-0XB0)	20%	400	25%	100	300		4
CPU 1214C DC/DC/DC (6ES7 214-1AG40-0XB0)	20%	400	25%	100	300		4
CPU 1214C DC/DC/Rly (6ES7 214-1HG40-0XB0)	20%	400	25%	100	300		4
CPU 1215C AC/DC/Rly (6ES7 215-1BG40-0XB0)	20%	400	25%	100	300		4
CPU 1215C DC/DC/DC (6ES7 215-1AG40-0XB0)	20%	400	25%	100	300		4
CPU 1215C DC/DC/Rly (6ES7 215-1HG40-0XB0)	20%	400	25%	100	300		4
CPU 1217C DC/DC/DC (6ES7 217-1AG40-0XB0)	20%	400	25%	100	300		4
CPU 1214FC DC/DC/DC (6ES7 214-1AF40-0XB0)	20%	400	25%	100	300		4
CPU 1214FC DC/DC/Rly (6ES7 214-1HF40-0XB0)	20%	400	25%	100	300		4
CPU 1215FC DC/DC/DC (6ES7 215-1AF40-0XB0)	20%	400	25%	100	300		4
CPU 1215FC DC/DC/Rly (6ES7 215-1HF40-0XB0)	20%	400	25%	100	300		4
CPU 1212FC DC/DC/DC (6ES7 212-1AF40-0XB0)	20%	400	25%	100	300		4
CPU 1212FC DC/DC/Rly (6ES7 212-1HF40-0XB0)	20%	400	25%	100	300		4

	Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
	Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)

1) Diese Zahl ist keine genaue Angabe, sondern nur eine Daumenregel.

Die Anzahl gilt für den eingeschwungenen Zustand, also ohne Bildwechsel. Daher darf die Mindestanzahl temporär niedriger sein; sobald die Zahl gegen 0 geht, beginnt das zyklische Polling und ist die CPU potentiell überlastet.

1) This is not an exact number, just a rule of thumb.

The number is valid for the engaged status, without screen change. This is why the minimum number may be lower temporarily; as soon as the number tends to 0, the cyclic polling begins and the CPU is potentially overloaded.

		Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
		Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)
1500-er CPUs	**FW-Stand**							
TIA-Portal V15								
SIPLUS CPUs sind nicht aufgeführt, sie basieren auf hier gelisteten CPUs								
SPILUS CPUs are not listed separately as they are based on the listed CPUs								
CPU 1505S (6ES7 672-5AC01-0YA0)	V2.5	30%	8000	25%	2000	6000	27	27
CPU 1505S FailSafe (6ES7 672-5FC01-0YA0)	V2.5	30%	8000	25%	2000	6000	27	27
CPU 1507S (6ES7 672-7AC01-0YA0)	V2.5	30%	8000	25%	2000	6000	41	41
CPU 1507S FailSafe (6ES7 672-7FC01-0YA0)	V2.5	30%	8000	25%	2000	6000	41	41
CPU 1510SP-1 PN (6ES7 510-1DJ01-0AB0)	V2.5	30%	2000	15%	300	1700	30	19
CPU 1510SP F-1 PN (6ES7 510-1SJ01-0AB0)	V2.5	30%	2000	15%	300	1700	30	19
CPU 1511-1 PN (6ES7 511-1AK01-0AB0)	V2.5	30%	2000	15%	300	1700	30	19
CPU 1511-1 PN (6ES7 511-1AK02-0AB0)	V2.5	30%	2000	15%	300	1700	30	19
CPU 1511C-1 PN (6ES7 511-1CK00-0AB0)	V2.5	30%	2000	15%	300	1700	30	19
CPU 1511F-1 PN (6ES7 511-1FK01-0AB0)	V2.5	30%	2000	15%	300	1700	30	19
CPU 1511F-1 PN (6ES7 511-1FK02-0AB0)	V2.5	30%	2000	15%	300	1700	30	19
CPU 1511T-1 PN (6ES7 511-1TK01-0AB0)	V2.5	30%	2000	15%	300	1700	30	19
CPU 1512C-1 PN (6ES7 512-1CK00-0AB0)	V2.5	30%	2000	15%	300	1700	41	27
CPU 1512C-1 PN (6ES7 512-1CK01-0AB0)	V2.5	30%	2000	15%	300	1700	41	27
CPU 1512SP F-1 PN (6ES7 512-1SK01-0AB0)	V2.5	30%	2000	15%	300	1700	41	27
CPU 1512SP-1 PN (6ES7 512-1DK01-0AB0)	V2.5	30%	2000	15%	300	1700	41	27
CPU 1513-1 PN (6ES7 513-1AL01-0AB0)	V2.5	30%	2000	15%	300	1700	41	27
CPU 1513-1 PN (6ES7 513-1AL02-0AB0)	V2.5	30%	2000	15%	300	1700	41	27
CPU 1513F-1 PN (6ES7 513-1FL01-0AB0)	V2.5	30%	2000	15%	300	1700	41	27
CPU 1513F-1 PN (6ES7 513-1FL02-0AB0)	V2.5	30%	2000	15%	300	1700	41	27
CPU 1515-2 PN (6ES7 515-2AM01-0AB0)	V2.5	30%	4000	25%	1000	3000	62	34
CPU 1515F-2 PN (6ES7 515-2FM01-0AB0)	V2.5	30%	4000	25%	1000	3000	62	34
CPU 1515T-2 PN (6ES7515-2TM01-0AB0)	V2.5	30%	4000	25%	1000	3000	62	34
CPU 1516-3 PN/DP (6ES7 516-3AN01-0AB0)	V2.5	30%	8000	25%	2000	6000	83	41
CPU 1516F-3 PN/DP (6ES7 516-3FN01-0AB0)	V2.5	30%	8000	25%	2000	6000	83	41
CPU 1516pro F-2 PN (6ES7 516-2GN00-0AB0)	V2.5	30%	8000	25%	2000	6000	41	41
CPU 1516pro-2 PN (6ES7 516-2PN00-0AB0)	V2.5	30%	8000	25%	2000	6000	41	41
CPU 1517-3 PN/DP (6ES7 517-3AP00-0AB0)	V2.5	30%	20000	15%	3000	17000	105	51
CPU 1517F-3 PN/DP (6ES7 517-3FP00-0AB0)	V2.5	30%	20000	15%	3000	17000	105	51
CPU 1517T-3 PN/DP (6ES7 517-3TP00-0AB0)	V2.5	30%	20000	15%	3000	17000	105	51
CPU 1517TF-3 PN/DP (6ES7 517-3UP00-0AB0)	V2.5	30%	20000	15%	3000	17000	105	51
CPU 1518-4 PN/DP (6ES7 518-4AP00-0AB0)	V2.5	30%	40000	15%	6000	34000	126	62
CPU 1518-4 PN/DP MFP (6ES7 518-4AX00-1AB0)	V2.5	30%	40000	15%	6000	34000	126	62
CPU 1518-4 PN/DP ODK (6ES7 518-4AP00-3AB0)	V2.5	30%	40000	15%	6000	34000	126	62

		Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
		Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)
CPU 1518F ODK-4 PN/DP (6ES7 518-4FP00-3AB0)	V2.5	30%	40000	15%	6000	34000	126	62
CPU 1518F-4 PN/DP (6ES7 518-4FP00-0AB0)	V2.5	30%	40000	15%	6000	34000	126	62
CPU 1518F-4 PN/DP MFP (6ES7 518-4FX00-1AB0)	V2.5	30%	40000	15%	6000	34000	126	62
1200-er CPUs								
TIA-Portal V15								
CPU 1211C AC/DC/Rly (6ES7 211-1BE40-0XB0)		20%	400	25%	100	300		6
CPU 1211C DC/DC/DC (6ES7 211-1AE40-0XB0)		20%	400	25%	100	300		6
CPU 1211C DC/DC/Rly (6ES7 211-1HE40-0XB0)		20%	400	25%	100	300		6
CPU 1212C AC/DC/Rly (6ES7 212-1BE40-0XB0)		20%	400	25%	100	300		6
CPU 1212C DC/DC/DC (6ES7 212-1AE40-0XB0)		20%	400	25%	100	300		6
CPU 1212C DC/DC/Rly (6ES7 212-1HE40-0XB0)		20%	400	25%	100	300		6
CPU 1214C AC/DC/Rly (6ES7 214-1BG40-0XB0)		20%	400	25%	100	300		6
CPU 1214C DC/DC/DC (6ES7 214-1AG40-0XB0)		20%	400	25%	100	300		6
CPU 1214C DC/DC/Rly (6ES7 214-1HG40-0XB0)		20%	400	25%	100	300		6
CPU 1215C AC/DC/Rly (6ES7 215-1BG40-0XB0)		20%	400	25%	100	300		6
CPU 1215C DC/DC/DC (6ES7 215-1AG40-0XB0)		20%	400	25%	100	300		6
CPU 1215C DC/DC/Rly (6ES7 215-1HG40-0XB0)		20%	400	25%	100	300		6
CPU 1217C DC/DC/DC (6ES7 217-1AG40-0XB0)		20%	400	25%	100	300		6
CPU 1214FC DC/DC/DC (6ES7 214-1AF40-0XB0)		20%	400	25%	100	300		6
CPU 1214FC DC/DC/Rly (6ES7 214-1HF40-0XB0)		20%	400	25%	100	300		6
CPU 1215FC DC/DC/DC (6ES7 215-1AF40-0XB0)		20%	400	25%	100	300		6
CPU 1215FC DC/DC/Rly (6ES7 215-1HF40-0XB0)		20%	400	25%	100	300		6
CPU 1212FC DC/DC/DC (6ES7 212-1AF40-0XB0)		20%	400	25%	100	300		6
CPU 1212FC DC/DC/Rly (6ES7 212-1HF40-0XB0)		20%	400	25%	100	300		6

		Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
		Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)

1) Diese Zahl ist keine genaue Angabe, sondern nur eine Daumenregel.

Die Anzahl gilt für den eingeschwungenen Zustand, also ohne Bildwechsel. Daher darf die Mindestanzahl temporär niedriger sein; sobald die Zahl gegen 0 geht, beginnt das zyklische Polling und ist die CPU potentiell überlastet.

1) This is not an exact number, just a rule of thumb.

The number is valid for the engaged status, without screen change. This is why the minimum number may be lower temporarily; as soon as the number tends to 0, the cyclic polling begins and the CPU is potentially overloaded.

		Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
		Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)
1500-er CPUs	**FW-Stand**							
TIA-Portal V15.1								
SIPLUS CPUs sind nicht aufgeführt, sie basieren auf hier gelisteten CPUs								
SPLUS CPUs are not listed separately as they are based on the listed CPUs								
CPU 1510SP F-1 PN (6ES7 510-1SJ01-0AB0)	V2.6	30%	2000	15%	300	1700	30	19
CPU 1507S (6ES7 672-7AC01-0YA0)	V2.6	30%	8000	25%	2000	6000	41	41
CPU 1507S FailSafe (6ES7 672-7FC01-0YA0)	V2.6	30%	8000	25%	2000	6000	41	41
CPU 1510SP-1 PN (6ES7 510-1DJ01-0AB0)	V2.6	30%	2000	15%	300	1700	30	19
CPU 1510SP-1 PN SIPLUS (6AG1 510-1DJ01-2AB0)	V2.6	30%	2000	15%	300	1700	30	19
CPU 1511-1 PN (6ES7 511-1AK01-0AB0)	V2.6	30%	2000	15%	300	1700	30	19
CPU 1511-1 PN (6ES7 511-1AK02-0AB0)	V2.6	30%	2000	15%	300	1700	30	19
CPU 1511-1 PN SIPLUS (6AG1 511-1AK01-2AB0)	V2.6	30%	2000	15%	300	1700	30	19
CPU 1511-1 PN SIPLUS (6AG1 511-1AK01-7AB0)	V2.6	30%	2000	15%	300	1700	30	19
CPU 1511C-1 PN (6ES7 511-1CK00-0AB0)	V2.6	30%	2000	15%	300	1700	30	19
CPU 1511C-1 PN (6ES7 511-1CK01-0AB0)	V2.6	30%	2000	15%	300	1700	30	19
CPU 1511F-1 PN (6ES7 511-1FK01-0AB0)	V2.6	30%	2000	15%	300	1700	30	19
CPU 1511F-1 PN (6ES7 511-1FK02-0AB0)	V2.6	30%	2000	15%	300	1700	30	19
CPU 1511T-1 PN (6ES7 511-1TK01-0AB0)	V2.6	30%	2000	15%	300	1700	30	19
CPU 1512C-1 PN (6ES7 512-1CK00-0AB0)	V2.6	30%	2000	15%	300	1700	41	27
CPU 1512SP-1 PN (6ES7 512-1DK01-0AB0)	V2.6	30%	2000	15%	300	1700	41	27
CPU 1513-1 PN (6ES7 513-1AL01-0AB0)	V2.6	30%	2000	15%	300	1700	41	27
CPU 1513-1 PN (6ES7 513-1AL02-0AB0)	V2.6	30%	2000	15%	300	1700	41	27
CPU 1513-1 PN SIPLUS (6AG1 513-1AL01-2AB0)	V2.6	30%	2000	15%	300	1700	41	27
CPU 1513-1 PN SIPLUS (6AG1 513-1AL01-7AB0)	V2.6	30%	2000	15%	300	1700	41	27
CPU 1513F-1 PN (6ES7 513-1FL02-0AB0)	V2.6	30%	2000	15%	300	1700	41	27
CPU 1513R-1 PN (6ES7 513-1RL00-0AB0)	V2.6	30%	2000	15%	300	1700	41	27
CPU 1515-2 PN (6ES7 515-2AM01-0AB0)	V2.6	30%	4000	25%	1000	3000	62	34
CPU 1515F-2 PN (6ES7 515-2FM01-0AB0)	V2.6	30%	4000	25%	1000	3000	62	34
CPU 1515R-2 PN (6ES7 515-2RM00-0AB0)	V2.6	30%	4000	25%	1000	3000	62	34
CPU 1516-3 PN/DP (6ES7 516-3AN01-0AB0)	V2.6	30%	8000	25%	2000	6000	83	41
CPU 1516-3 PN/DP SIPLUS (6AG1 516-3AN01-2AB0)	V2.6	30%	8000	25%	2000	6000	83	41
CPU 1516-3 PN/DP SIPLUS (6AG1 516-3AN01-7AB0)	V2.6	30%	8000	25%	2000	6000	83	41
CPU 1516F-3 PN/DP (6ES7 516-3FN01-0AB0)	V2.6	30%	8000	25%	2000	6000	83	41
CPU 1516pro F-2 PN (6ES7 516-2GN00-0AB0)	V2.6	30%	8000	25%	2000	6000	41	41
CPU 1516pro-2 PN (6ES7 516-2PN00-0AB0)	V2.6	30%	8000	25%	2000	6000	41	41
CPU 1517-3 PN/DP (6ES7 517-3AP00-0AB0)	V2.6	30%	20000	15%	3000	17000	105	51

		Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
		Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)
CPU 1517F-3 PN/DP (6ES7 517-3FP00-0AB0)	V2.6	30%	20000	15%	3000	17000	105	51
CPU 1517H-3PN (6ES7 517-3HP00-0AB0)	V2.6	30%	20000	15%	3000	17000	105	51
CPU 1517T-3 PN/DP (6ES7 517-3TP00-0AB0)	V2.6	30%	20000	15%	3000	17000	105	51
CPU 1517TF-3 PN/DP (6ES7 517-3UP00-0AB0)	V2.6	30%	20000	15%	3000	17000	105	51
CPU 1518-4 PN/DP (6ES7 518-4AP00-0AB0)	V2.6	30%	40000	15%	6000	34000	126	62
CPU 1518-4 PN/DP MFP (6ES7 518-4AX00-1AB0)	V2.6	30%	40000	15%	6000	34000	126	62
CPU 1518-4 PN/DP ODK (6ES7 518-4AP00-3AB0)	V2.6	30%	40000	15%	6000	34000	126	62
CPU 1518-4 PN/DP SIPLUS (6AG1 518-4AP00-4AB0)	V2.6	30%	40000	15%	6000	34000	126	62
CPU 1518F ODK-4 PN/DP (6ES7 518-4FP00-3AB0)	V2.6	30%	40000	15%	6000	34000	126	62
CPU 1518F-4 PN/DP (6ES7 518-4FP00-0AB0)	V2.6	30%	40000	15%	6000	34000	126	62
CPU 1518F-4 PN/DP MFP (6ES7 518-4FX00-1AB0)	V2.6	30%	40000	15%	6000	34000	126	62
S7-1516T-3 PN/DP(6ES7 516-3TN00-0AB0)	V2.6	30%	8000	25%	2000	6000	83	41
S7-1516TF-3 PN/DP(6ES7 516-3UN00-0AB0)	V2.6	30%	8000	25%	2000	6000	83	41
S7-1511T F-1PN(6ES7 511-1UK01-0AB0)	V2.6	30%	2000	15%	300	1700	30	19
S7-1515T(6ES7 515-2TM01-0AB0)	V2.6	30%	4000	25%	1000	3000	62	34
S7-1515TF-2 PN (6ES7 515-2UM01-0AB0)	V2.6	30%	4000	25%	1000	3000	62	34
1200-er CPUs								
TIA-Portal V15.1								
CPU 1211C AC/DC/Rly (6ES7 211-1BE40-0XB0)		20%	400	25%	100	300		6
CPU 1211C DC/DC/DC (6ES7 211-1AE40-0XB0)		20%	400	25%	100	300		6
CPU 1211C DC/DC/Rly (6ES7 211-1HE40-0XB0)		20%	400	25%	100	300		6
CPU 1212C AC/DC/Rly (6ES7 212-1BE40-0XB0)		20%	400	25%	100	300		6
CPU 1212C DC/DC/DC (6ES7 212-1AE40-0XB0)		20%	400	25%	100	300		6
CPU 1212C DC/DC/Rly (6ES7 212-1HE40-0XB0)		20%	400	25%	100	300		6
CPU 1214C AC/DC/Rly (6ES7 214-1BG40-0XB0)		20%	400	25%	100	300		6
CPU 1214C DC/DC/DC (6ES7 214-1AG40-0XB0)		20%	400	25%	100	300		6
CPU 1214C DC/DC/Rly (6ES7 214-1HG40-0XB0)		20%	400	25%	100	300		6
CPU 1215C AC/DC/Rly (6ES7 215-1BG40-0XB0)		20%	400	25%	100	300		6
CPU 1215C DC/DC/DC (6ES7 215-1AG40-0XB0)		20%	400	25%	100	300		6
CPU 1215C DC/DC/Rly (6ES7 215-1HG40-0XB0)		20%	400	25%	100	300		6
CPU 1217C DC/DC/DC (6ES7 217-1AG40-0XB0)		20%	400	25%	100	300		6
CPU 1214FC DC/DC/DC (6ES7 214-1AF40-0XB0)		20%	400	25%	100	300		6
CPU 1214FC DC/DC/Rly (6ES7 214-1HF40-0XB0)		20%	400	25%	100	300		6
CPU 1215FC DC/DC/DC (6ES7 215-1AF40-0XB0)		20%	400	25%	100	300		6
CPU 1215FC DC/DC/Rly (6ES7 215-1HF40-0XB0)		20%	400	25%	100	300		6
CPU 1212FC DC/DC/DC (6ES7 212-1AF40-0XB0)		20%	400	25%	100	300		6
CPU 1212FC DC/DC/Rly (6ES7 212-1HF40-0XB0)		20%	400	25%	100	300		6

		Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU	Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)	Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
		Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU	Maximale Anzahl verbundener WinCC Professional RT *mit* CP auf PLC-Seite (inklusive 1x ES)	Maximale Anzahl verbundener WinCC Professional RT *ohne* CP auf PLC-Seite (inklusive 1x ES)

1) Diese Zahl ist keine genaue Angabe, sondern nur eine Daumenregel.

Die Anzahl gilt für den eingeschwungenen Zustand, also ohne Bildwechsel. Daher darf die Mindestanzahl temporär niedriger sein; sobald die Zahl gegen 0 geht, beginnt das zyklische Polling und ist die CPU potentiell überlastet.

1) This is not an exact number, just a rule of thumb.

The number is valid for the engaged status, without screen change. This is why the minimum number may be lower temporarily; as soon as the number tends to 0, the cyclic polling begins and the CPU is potentially overloaded.

		Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU		Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)		Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
		Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU		Maximale Anzahl verbundener WinCC Professional RT mit CP auf PLC-Seite (inklusive 1x ES)		Maximale Anzahl verbundener WinCC Professional RT ohne CP auf PLC-Seite (inklusive 1x ES)
1500-er CPUs	**FW-Stand**									
TIA-Portal V16										
SIPLUS CPUs sind nicht aufgeführt, sie basieren auf hier gelisteten CPUs						TechDat: Connection Station Limit (PLC + CPs)		TechDat: Connection PLC Limit		
SPLUS CPUs are not listed separately as they										
Software Controller										
CPU1505SP FailSafe (6ES7 672-5SC11-0YA0)	V2.8	30%	8000	25%	2000	6000	88	27	88	27
CPU1505SP (6ES7 672-5DC11-0YA0)	V2.8	30%	8000	25%	2000	6000	88	27	88	27
CPU1505SP T (6ES7 672-5VC11-0YA0)	V2.8	30%	8000	25%	2000	6000	88	27	88	27
CPU1505SP TF (6ES7 672-5WC11-0YA0)	V2.8	30%	8000	25%	2000	6000	88	27	88	27
CPU 1507S FailSafe (6ES7 672-7FC01-0YA0)	V2.8	30%	8000	25%	2000	6000	128	41	128	41
CPU 1507S (6ES7 672-7AC01-0YA0)	V2.8	30%	8000	25%	2000	6000	128	41	128	41
CPU 1508S FailSafe (6ES7 672-8FC01-0YA0)	V2.8	30%	8000	25%	2000	6000	128	41	128	41
CPU 1508S (6ES7 672-8AC01-0YA0)	V2.8	30%	8000	25%	2000	6000	128	41	128	41
ET 200 Controller										
CPU 1510SP-1 PN (6ES7 510-1DJ01-0AB0)	V2.8	30%	2000	15%	300	1700	96	30	64	19
CPU 1510SP F-1 PN (6ES7 510-1SJ01-0AB0)	V2.8	30%	2000	15%	300	1700	96	30	64	19
CPU 1512SP-1 PN (6ES7 512-1DK01-0AB0)	V2.8	30%	2000	15%	300	1700	128	41	88	27
CPU 1512SP F-1 PN (6ES7 512-1SK01-0AB0)	V2.8	30%	2000	15%	300	1700	128	41	88	27
CPU 1513pro-2 PN (6ES7 513-2PL00-0AB0)	V2.8	30%	2000	15%	300	1700	128	41	128	41
CPU 1513pro F-2 PN (6ES7 513-2GL00-0AB0)	V2.8	30%	2000	15%	300	1700	128	41	128	41
CPU 1516pro-2 PN (6ES7 516-2PN00-0AB0)	V2.8	30%	8000	25%	2000	6000	128	41	128	41
CPU 1516pro F-2 PN (6ES7 516-2GN00-0AB0)	V2.8	30%	8000	25%	2000	6000	128	41	128	41
S7-1500 Compact Controller										
CPU 1511C-1 PN (6ES7 511-1CK00-0AB0)	V2.8	30%	2000	15%	300	1700	96	30	64	19
CPU 1511C-1 PN (6ES7 511-1CK01-0AB0)	V2.8	30%	2000	15%	300	1700	96	30	64	19
CPU 1512C-1 PN (6ES7 512-1CK00-0AB0)	V2.8	30%	2000	15%	300	1700	128	41	88	27
CPU 1512C-1 PN (6ES7 512-1CK01-0AB0)	V2.8	30%	2000	15%	300	1700	128	41	88	27
S7-1500 Controller										
CPU 1511-1 PN (6ES7 511-1AK01-0AB0)	V2.8	30%	2000	15%	300	1700	96	30	64	19
CPU 1511-1 PN (6ES7 511-1AK02-0AB0)	V2.8	30%	2000	15%	300	1700	96	30	64	19
CPU 1511F-1 PN (6ES7 511-1FK01-0AB0)	V2.8	30%	2000	15%	300	1700	96	30	64	19
CPU 1511F-1 PN (6ES7 511-1FK02-0AB0)	V2.8	30%	2000	15%	300	1700	96	30	64	19
CPU 1513-1 PN (6ES7 513-1AL01-0AB0)	V2.8	30%	2000	15%	300	1700	128	41	88	27
CPU 1513-1 PN (6ES7 513-1AL02-0AB0)	V2.8	30%	2000	15%	300	1700	128	41	88	27
CPU 1513F-1 PN (6ES7 513-1FL01-0AB0)	V2.8	30%	2000	15%	300	1700	128	41	88	27
CPU 1513F-1 PN (6ES7 513-1FL02-0AB0)	V2.8	30%	2000	15%	300	1700	128	41	88	27
CPU 1515-2 PN (6ES7 515-2AM01-0AB0)	V2.8	30%	4000	25%	1000	3000	192	62	108	34
CPU 1515-2 PN (6ES7 515-2AM02-0AB0)	V2.8	30%	4000	25%	1000	3000	192	62	108	34
CPU 1515F-2 PN (6ES7 515-2FM01-0AB0)	V2.8	30%	4000	25%	1000	3000	192	62	108	34
CPU 1515F-2 PN (6ES7 515-2FM02-0AB0)	V2.8	30%	4000	25%	1000	3000	192	62	108	34
CPU 1516-3 PN/DP (6ES7 516-3AN01-0AB0)	V2.8	30%	8000	25%	2000	6000	256	83	128	41
CPU 1516-3 PN/DP (6ES7 516-3AN02-0AB0)	V2.8	30%	8000	25%	2000	6000	256	83	128	41
CPU 1516F-3 PN/DP (6ES7 516-3FN01-0AB0)	V2.8	30%	8000	25%	2000	6000	256	83	128	41
CPU 1516F-3 PN/DP (6ES7 516-3FN02-0AB0)	V2.8	30%	8000	25%	2000	6000	256	83	128	41
CPU 1517-3 PN/DP (6ES7 517-3AP00-0AB0) ORIG	V2.8	30%	20000	15%	3000	17000	320	105	288	94
CPU 1517-3 PN/DP (6ES7 517-3AP00-0AB0)	V2.8	30%	20000	15%	3000	17000	320	105	288	94
CPU 1517F-3 PN/DP (6ES7 517-3FP00-0AB0)	V2.8	30%	20000	15%	3000	17000	320	105	288	94
CPU 1518-4 PN/DP (6ES7 518-4AP00-0AB0)	V2.8	30%	40000	15%	6000	34000	384	126	320	105
CPU 1518F-4 PN/DP (6ES7 518-4FP00-0AB0)	V2.8	30%	40000	15%	6000	34000	384	126	320	105

		Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU		Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)		Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
		Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU		Maximale Anzahl verbundener WinCC Professional RT mit CP auf PLC-Seite (inklusive 1x ES)		Maximale Anzahl verbundener WinCC Professional RT ohne CP auf PLC-Seite (inklusive 1x ES)
CPU 1518-4 PN/DP ODK (6ES7 518-4AP00-3AB0)	V2.8	30%	40000	15%	6000	34000	384	126	320	105
CPU 1518F-4 PN/DP ODK (6ES7 518-4FP00-3AB0)	V2.8	30%	40000	15%	6000	34000	384	126	320	105
CPU 1518-4 PN/DP MFP (6ES7 518-4AX00-1AB0)	V2.8	30%	40000	15%	6000	34000	384	126	320	105
CPU 1518F-4 PN/DP MFP (6ES7 518-4FX00-1AB0)	V2.8	30%	40000	15%	6000	34000	384	126	320	105
S7-1500 Technology Controller										
CPU 1511T-1 PN (6ES7 511-1TK01-0AB0)	V2.8	30%	2000	15%	300	1700	96	30	64	19
S7-1511TF-1PN (6ES7 511-1UK01-0AB0)	V2.8	30%	2000	15%	300	1700	96	30	64	19
S7-1515T-2 PN (6ES7 515-2TM01-0AB0)	V2.8	30%	4000	25%	1000	3000	192	62	108	34
S7-1515TF-2 PN (6ES7 515-2UM01-0AB0)	V2.8	30%	4000	25%	1000	3000	192	62	108	34
S7-1516T-3 PN/DP(6ES7 516-3TN00-0AB0)	V2.8	30%	8000	25%	2000	6000	256	83	128	41
S7-1516TF-3 PN/DP(6ES7 516-3UN00-0AB0)	V2.8	30%	8000	25%	2000	6000	256	83	128	41
CPU 1517T-3 PN/DP (6ES7 517-3TP00-0AB0)	V2.8	30%	20000	15%	3000	17000	320	105	288	94
CPU 1517TF-3 PN/DP (6ES7 517-3UP00-0AB0)	V2.8	30%	20000	15%	3000	17000	320	105	288	94
S7-1500 Redundent Controller										
CPU 1513R-1 PN (6ES7 513-1RL00-0AB0)	V2.8	30%	2000	15%	300	1700	88	27	88	27
CPU 1515R-2 PN (6ES7 515-2RM00-0AB0)	V2.8	30%	4000	25%	1000	3000	108	34	108	34
CPU 1517H-3PN (6ES7 517-3HP00-0AB0)	V2.8	30%	20000	15%	3000	17000	288	94	288	94
1200-er CPUs										
TIA-Portal V16										
CPU 1211C AC/DC/Rly (6ES7 211-1BE40-0XB0)		20%	400	25%	100	300				6
CPU 1211C DC/DC/DC (6ES7 211-1AE40-0XB0)		20%	400	25%	100	300				6
CPU 1211C DC/DC/Rly (6ES7 211-1HE40-0XB0)		20%	400	25%	100	300				6
CPU 1212C AC/DC/Rly (6ES7 212-1BE40-0XB0)		20%	400	25%	100	300				6
CPU 1212C DC/DC/DC (6ES7 212-1AE40-0XB0)		20%	400	25%	100	300				6
CPU 1212C DC/DC/Rly (6ES7 212-1HE40-0XB0)		20%	400	25%	100	300				6
CPU 1214C AC/DC/Rly (6ES7 214-1BG40-0XB0)		20%	400	25%	100	300				6
CPU 1214C DC/DC/DC (6ES7 214-1AG40-0XB0)		20%	400	25%	100	300				6
CPU 1214C DC/DC/Rly (6ES7 214-1HG40-0XB0)		20%	400	25%	100	300				6
CPU 1215C AC/DC/Rly (6ES7 215-1BG40-0XB0)		20%	400	25%	100	300				6
CPU 1215C DC/DC/DC (6ES7 215-1AG40-0XB0)		20%	400	25%	100	300				6
CPU 1215C DC/DC/Rly (6ES7 215-1HG40-0XB0)		20%	400	25%	100	300				6
CPU 1217C DC/DC/DC (6ES7 217-1AG40-0XB0)		20%	400	25%	100	300				6
CPU 1214FC DC/DC/DC (6ES7 214-1AF40-0XB0)		20%	400	25%	100	300				6
CPU 1214FC DC/DC/Rly (6ES7 214-1HF40-0XB0)		20%	400	25%	100	300				6
CPU 1215FC DC/DC/DC (6ES7 215-1AF40-0XB0)		20%	400	25%	100	300				6
CPU 1215FC DC/DC/Rly (6ES7 215-1HF40-0XB0)		20%	400	25%	100	300				6
CPU 1217FC DC/DC/DC (6ES7 217-1AF40-0XB0)		20%	400	25%	100	300				6
CPU 1212FC DC/DC/Rly (6ES7 212-1HF40-0XB0)		20%	400	25%	100	300				6

		Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU		Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)		Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
		Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU		Maximale Anzahl verbundener WinCC Professional RT mit CP auf PLC-Seite (inklusive 1x ES)		Maximale Anzahl verbundener WinCC Professional RT ohne CP auf PLC-Seite (inklusive 1x ES)

1) Diese Zahl ist keine genaue Angabe, sondern nur eine Daumenregel.

Die Anzahl gilt für den eingeschwungenen Zustand, also ohne Bildwechsel. Daher darf die Mindestanzahl temporär niedriger sein; sobald die Zahl gegen 0 geht, beginnt das zyklische Polling und ist die CPU potentiell überlastet.

1) This is not an exact number, just a rule of thumb.

The number is valid for the engaged status,

		Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU		Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)		Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
		Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU		Maximale Anzahl verbundener WinCC Professional RT mit CP auf PLC-Seite (inklusive 1x ES)		Maximale Anzahl verbundener WinCC Professional RT ohne CP auf PLC-Seite (inklusive 1x ES)
1500-er CPUs	**FW-Stand**									
TIA-Portal V17										
SIPLUS CPUs sind nicht aufgeführt, sie basieren auf hier gelisteten CPUs						TechData: Connection Station Limit (PLC + CPs		TechData: Connection PLC Limit		
SPLUS CPUs are not listed separately as they are based on the listed CPUs										
Software Controller										
CPU1505SP FailSafe (6ES7 672-5SC11-0YA0)	V2.9	30%	8000	25%	2000	6000	88	27	88	27
CPU1505SP (6ES7 672-5DC11-0YA0)	V2.9	30%	8000	25%	2000	6000	88	27	88	27
CPU1505SP T (6ES7 672-5VC11-0YA0)	V2.9	30%	8000	25%	2000	6000	88	27	88	27
CPU1505SP TF (6ES7 672-5WC11-0YA0)	V2.9	30%	8000	25%	2000	6000	88	27	88	27
CPU 1507S FailSafe (6ES7 672-7FC01-0YA0)	V2.9	30%	8000	25%	2000	6000	128	41	128	41
CPU 1507S (6ES7 672-7AC01-0YA0)	V2.9	30%	8000	25%	2000	6000	128	41	128	41
CPU 1508S FailSafe (6ES7 672-8FC01-0YA0)	V2.9	30%	8000	25%	2000	6000	128	41	128	41
CPU 1508S (6ES7 672-8AC01-0YA0)	V2.9	30%	8000	25%	2000	6000	128	41	128	41
ET 200 Controller										
CPU 1510SP-1 PN (6ES7 510-1DJ01-0AB0)	V2.9	30%	2000	15%	300	1700	96	30	64	19
CPU 1510SP F-1 PN (6ES7 510-1SJ01-0AB0)	V2.9	30%	2000	15%	300	1700	96	30	64	19
CPU 1512SP-1 PN (6ES7 512-1DK01-0AB0)	V2.9	30%	2000	15%	300	1700	128	41	88	27
CPU 1512SP F-1 PN (6ES7 512-1SK01-0AB0)	V2.9	30%	2000	15%	300	1700	128	41	88	27
CPU 1513pro-2 PN (6ES7 513-2PL00-0AB0)	V2.9	30%	2000	15%	300	1700	128	41	128	41
CPU 1513pro F-2 PN (6ES7 513-2GL00-0AB0)	V2.9	30%	2000	15%	300	1700	128	41	128	41
CPU 1516pro-2 PN (6ES7 516-2PN00-0AB0)	V2.9	30%	8000	25%	2000	6000	128	41	128	41
CPU 1516pro F-2 PN (6ES7 516-2GN00-0AB0)	V2.9	30%	8000	25%	2000	6000	128	41	128	41
S7-1500 Compact Controller										
CPU 1511C-1 PN (6ES7 511-1CK00-0AB0)	V2.9	30%	2000	15%	300	1700	96	30	64	19
CPU 1511C-1 PN (6ES7 511-1CK01-0AB0)	V2.9	30%	2000	15%	300	1700	96	30	64	19
CPU 1512C-1 PN (6ES7 512-1CK00-0AB0)	V2.9	30%	2000	15%	300	1700	128	41	88	27
CPU 1512C-1 PN (6ES7 512-1CK01-0AB0)	V2.9	30%	2000	15%	300	1700	128	41	88	27
S7-1500 Controller										
CPU 1511-1 PN (6ES7 511-1AK01-0AB0)	V2.9	30%	2000	15%	300	1700	96	30	64	19
CPU 1511-1 PN (6ES7 511-1AK02-0AB0)	V2.9	30%	2000	15%	300	1700	96	30	64	19
CPU 1511F-1 PN (6ES7 511-1FK01-0AB0)	V2.9	30%	2000	15%	300	1700	96	30	64	19
CPU 1511F-1 PN (6ES7 511-1FK02-0AB0)	V2.9	30%	2000	15%	300	1700	96	30	64	19
CPU 1513-1 PN (6ES7 513-1AL01-0AB0)	V2.9	30%	2000	15%	300	1700	128	41	88	27
CPU 1513-1 PN (6ES7 513-1AL02-0AB0)	V2.9	30%	2000	15%	300	1700	128	41	88	27
CPU 1513F-1 PN (6ES7 513-1FL01-0AB0)	V2.9	30%	2000	15%	300	1700	128	41	88	27
CPU 1513F-1 PN (6ES7 513-1FL02-0AB0)	V2.9	30%	2000	15%	300	1700	128	41	88	27
CPU 1515-2 PN (6ES7 515-2AM01-0AB0)	V2.9	30%	4000	25%	1000	3000	192	62	108	34
CPU 1515-2 PN (6ES7 515-2AM02-0AB0)	V2.9	30%	4000	25%	1000	3000	192	62	108	34
CPU 1515F-2 PN (6ES7 515-2FM01-0AB0)	V2.9	30%	4000	25%	1000	3000	192	62	108	34
CPU 1515F-2 PN (6ES7 515-2FM02-0AB0)	V2.9	30%	4000	25%	1000	3000	192	62	108	34
CPU 1516-3 PN/DP (6ES7 516-3AN01-0AB0)	V2.9	30%	8000	25%	2000	6000	256	83	128	41
CPU 1516-3 PN/DP (6ES7 516-3AN02-0AB0)	V2.9	30%	8000	25%	2000	6000	256	83	128	41
CPU 1516F-3 PN/DP (6ES7 516-3FN01-0AB0)	V2.9	30%	8000	25%	2000	6000	256	83	128	41

		Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU		Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)		Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
		Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU		Maximale Anzahl verbundener WinCC Professional RT mit CP auf PLC-Seite (inklusive 1x ES)		Maximale Anzahl verbundener WinCC Professional RT ohne CP auf PLC-Seite (inklusive 1x ES)
CPU 1516F-3 PN/DP (6ES7 516-3FN02-0AB0)	V2.9	30%	8000	25%	2000	6000	256	83	128	41
CPU 1517-3 PN/DP (6ES7 517-3AP00-0AB0)	V2.9	30%	20000	15%	3000	17000	320	105	288	94
CPU 1517F-3 PN/DP (6ES7 517-3FP00-0AB0)	V2.9	30%	20000	15%	3000	17000	320	105	288	94
CPU 1518-4 PN/DP (6ES7 518-4AP00-0AB0)	V2.9	30%	40000	15%	6000	34000	384	126	320	105
CPU 1518F-4 PN/DP (6ES7 518-4FP00-0AB0)	V2.9	30%	40000	15%	6000	34000	384	126	320	105
CPU 1518-4 PN/DP ODK (6ES7 518-4AP00-3AB0)	V2.9	30%	40000	15%	6000	34000	384	126	320	105
CPU 1518F-4 PN/DP ODK (6ES7 518-4FP00-3AB0)	V2.9	30%	40000	15%	6000	34000	384	126	320	105
CPU 1518-4 PN/DP MFP (6ES7 518-4AX00-1AB0)	V2.9	30%	40000	15%	6000	34000	384	126	320	105
CPU 1518F-4 PN/DP MFP (6ES7 518-4FX00-1AB0)	V2.9	30%	40000	15%	6000	34000	384	126	320	105
S7-1500 Technology Controller										
CPU 1511T-1 PN (6ES7 511-1TK01-0AB0)	V2.9	30%	2000	15%	300	1700	96	30	64	19
S7-1511TF-1PN (6ES7 511-1UK01-0AB0)	V2.9	30%	2000	15%	300	1700	96	30	64	19
S7-1515T-2 PN (6ES7 515-2TM01-0AB0)	V2.9	30%	4000	25%	1000	3000	192	62	108	34
S7-1515TF-2 PN (6ES7 515-2UM01-0AB0)	V2.9	30%	4000	25%	1000	3000	192	62	108	34
S7-1516T-3 PN/DP(6ES7 516-3TN00-0AB0)	V2.9	30%	8000	25%	2000	6000	256	83	128	41
S7-1516TF-3 PN/DP(6ES7 516-3UN00-0AB0)	V2.9	30%	8000	25%	2000	6000	256	83	128	41
CPU 1517T-3 PN/DP (6ES7 517-3TP00-0AB0)	V2.9	30%	20000	15%	3000	17000	320	105	288	94
CPU 1517TF-3 PN/DP (6ES7 517-3UP00-0AB0)	V2.9	30%	20000	15%	3000	17000	320	105	288	94
CPU 1518T-4 PN/DP (6ES7 518-4TP00-0AB0)	V2.9	30%	40000	15%	6000	34000	384	126	320	105
CPU 1518TF-4 PN/DP (6ES7 518-4UP00-0AB0)	V2.9	30%	40000	15%	6000	34000	384	126	320	105
S7-1500 Redundent Controller										
CPU 1513R-1 PN (6ES7 513-1RL00-0AB0)	V2.9	30%	2000	15%	300	1700	88	27	88	27
CPU 1515R-2 PN (6ES7 515-2RM00-0AB0)	V2.9	30%	4000	25%	1000	3000	108	34	108	34
CPU 1517H-3PN (6ES7 517-3HP00-0AB0)	V2.9	30%	20000	15%	3000	17000	288	94	288	94
CPU 1518HF-4 PN (6ES7 518-4JP00-0AB0)	V2.9	30%	40000	15%	6000	34000	320	105	320	105
1200-er CPUs										
TIA-Portal V17										
CPU 1211C AC/DC/Rly (6ES7 211-1BE40-0XB0)		20%	400	25%	100	300				6
CPU 1211C DC/DC/DC (6ES7 211-1AE40-0XB0)		20%	400	25%	100	300				6
CPU 1211C DC/DC/Rly (6ES7 211-1HE40-0XB0)		20%	400	25%	100	300				6
CPU 1212C AC/DC/Rly (6ES7 212-1BE40-0XB0)		20%	400	25%	100	300				6
CPU 1212C DC/DC/DC (6ES7 212-1AE40-0XB0)		20%	400	25%	100	300				6
CPU 1212C DC/DC/Rly (6ES7 212-1HE40-0XB0)		20%	400	25%	100	300				6
CPU 1214C AC/DC/Rly (6ES7 214-1BG40-0XB0)		20%	400	25%	100	300				6
CPU 1214C DC/DC/DC (6ES7 214-1AG40-0XB0)		20%	400	25%	100	300				6
CPU 1214C DC/DC/Rly (6ES7 214-1HG40-0XB0)		20%	400	25%	100	300				6
CPU 1215C AC/DC/Rly (6ES7 215-1BG40-0XB0)		20%	400	25%	100	300				6
CPU 1215C DC/DC/DC (6ES7 215-1AG40-0XB0)		20%	400	25%	100	300				6
CPU 1215C DC/DC/Rly (6ES7 215-1HG40-0XB0)		20%	400	25%	100	300				6
CPU 1217C DC/DC/DC (6ES7 217-1AG40-0XB0)		20%	400	25%	100	300				6
CPU 1214FC DC/DC/DC (6ES7 214-1AF40-0XB0)		20%	400	25%	100	300				6
CPU 1214FC DC/DC/Rly (6ES7 214-1HF40-0XB0)		20%	400	25%	100	300				6
CPU 1215FC DC/DC/DC (6ES7 215-1AF40-0XB0)		20%	400	25%	100	300				6
CPU 1215FC DC/DC/Rly (6ES7 215-1HF40-0XB0)		20%	400	25%	100	300				6

		Communications budget	Max. number of attribute references	Reserve in percent for screen change	Min. number "free PLC Attributes" 1)	Number of tags cyclically logged on simultaneously per S7plus CPU		Maximum number of connected WinCC Professional RTs with CP on PLC side (including 1x ES)		Maximum number of connected WinCC Professional RTs without CP on PLC side (including 1x ES)
		Kommunikations-budget	Max. Anzahl Attribut-Referenzen	Reserve in Prozent für Bildwechsel	Mindestanzahl "free PLC Attributes" 1)	Anzahl gleichzeitig zyklisch angemeldeter Tags pro S7plus CPU		Maximale Anzahl verbundener WinCC Professional RT mit CP auf PLC-Seite (inklusive 1x ES)		Maximale Anzahl verbundener WinCC Professional RT ohne CP auf PLC-Seite (inklusive 1x ES)
CPU 1212FC DC/DC/DC (6ES7 212-1AF40-0XB0)		20%	400	25%	100	300				6
CPU 1212FC DC/DC/Rly (6ES7 212-1HF40-0XB0)		20%	400	25%	100	300				6

1) Diese Zahl ist keine genaue Angabe, sondern nur eine Daumenregel.

Die Anzahl gilt für den eingeschwungenen Zustand, also ohne Bildwechsel. Daher darf die Mindestanzahl temporär niedriger sein; sobald die Zahl gegen 0 geht, beginnt das zyklische Polling und ist die CPU potentiell überlastet.

1) This is not an exact number, just a rule of thumb.

The number is valid for the engaged status, without